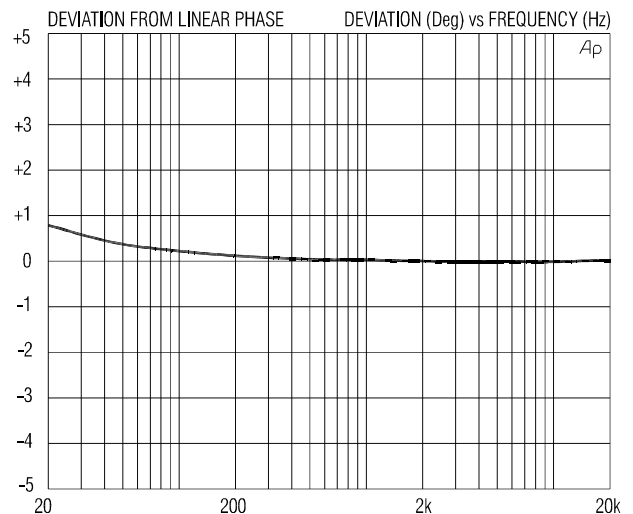
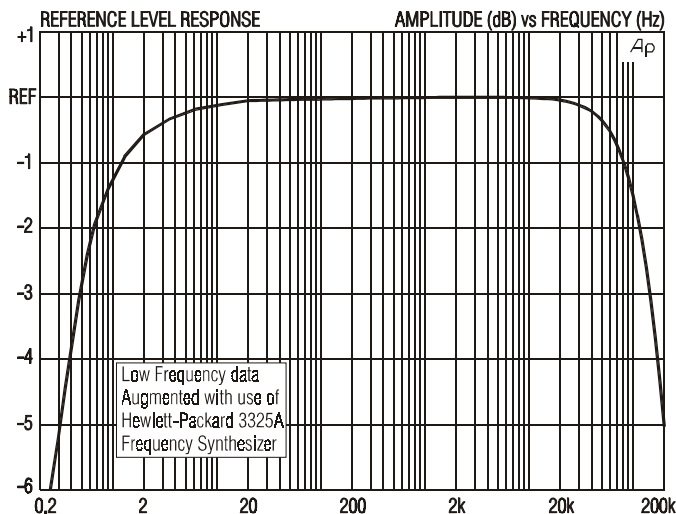
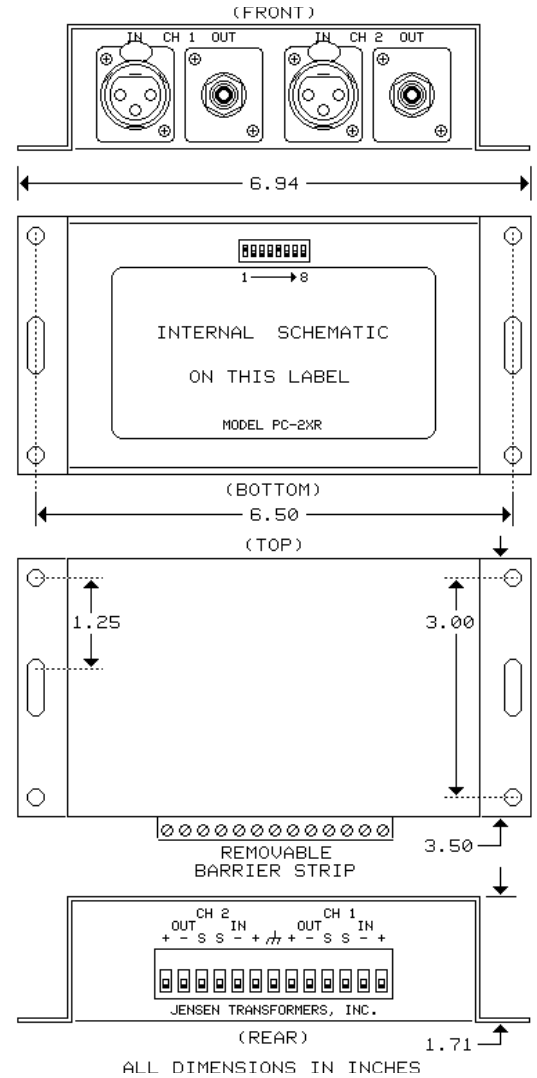
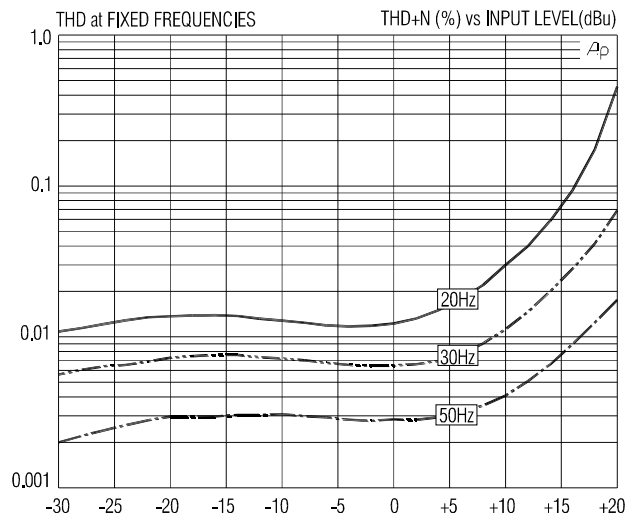
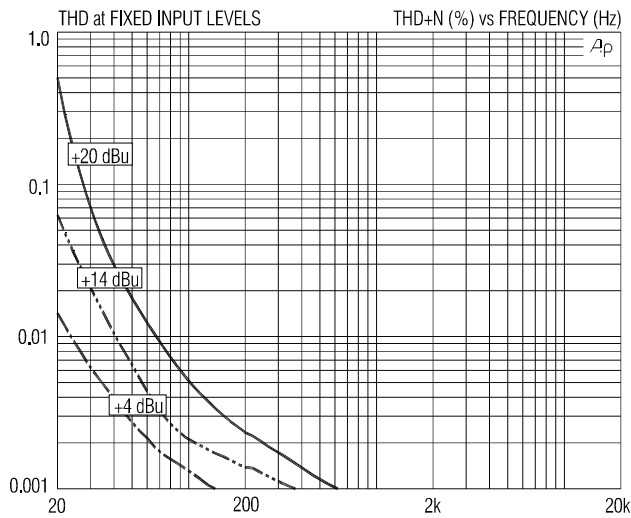


DUAL "PRO TO CONSUMER" CONVERTER PROVIDES GROUND ISOLATION AND LEVEL CONVERSION

- High CMRR when driven by balanced or unbalanced sources
- Converts nominal +4 dBu to nominal -10 dBV signal levels
- Handles levels up to +21 dBu at 20 Hz or +25 dBu at 30 Hz
- Wide bandwidth: -3dB at 0.5 Hz and 145 kHz
- Integral ground option switches solve other equipment problems

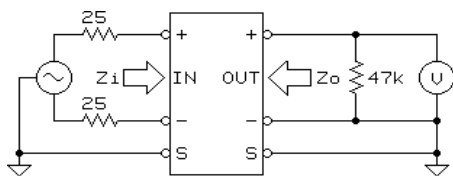
These units allow connection of balanced line level signals to consumer level unbalanced inputs while also providing isolation to eliminate "ground loops." Internal Faraday and magnetic shields reject RFI and hum pickup. Output cables should be kept under 10 feet (3 meters) to preserve high frequency bandwidth.



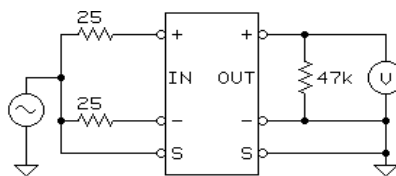


PC-2XR SPECIFICATIONS (all levels are input unless noted)

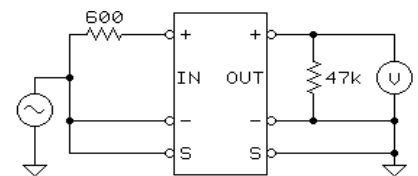
PARAMETER	CONDITIONS	MINIMUM	TYPICAL	MAXIMUM
Input impedance, Zi	1 kHz, +4 dBu, test circuit 1	32 kΩ	33.9 kΩ	36 kΩ
Voltage gain	1 kHz, +4 dBu, test circuit 1	-14.0 dB	-13.6 dB	
Magnitude response, ref 1 kHz	20 Hz, +4 dBu, test circuit 1	-0.15 dB	-0.04 dB	±0.0 dB
	20 kHz, +4 dBu, test circuit 1	-0.15 dB	-0.03 dB	+0.1 dB
Deviation from linear phase (DLP)	20 Hz to 20 kHz, +4 dBu, test circuit 1		+0.6/-0.1°	±2.0°
Distortion (THD)	1 kHz, +4 dBu, test circuit 1		<0.001%	
	20 Hz, +4 dBu, test circuit 1		0.015%	0.05%
Maximum 20 Hz input level	1% THD, test circuit 1	+19 dBu	+21 dBu	
Common-mode rejection ratio (CMRR) 50 Ω balanced / 600 Ω unbalanced source	60 Hz, test circuit 2 / 3		120 / 90 dB	
	3 kHz, test circuit 2 / 3	70 dB	85 / 55 dB	
Allowable source impedance	(output impedance of device connected to ISO-MAX input)	0 Ω	600 Ω	2 kΩ
Allowable load impedance	(input impedance of device connected to ISO-MAX output)	5 kΩ	47 kΩ	∞
Allowable load capacitance	(cable and input capacitance loading the ISO-MAX output)	0	150 pF	450 pF
Output impedance, Zo	1 kHz, test circuit 1		225 Ω	
DC resistances	Input		2.5 kΩ	
	Output		92 Ω	
Capacitances	1 kHz, input to output shield		120 pF	
	1 kHz, output to output shield		115 pF	
Temperature range	operation or storage	0° C		70° C
Input to Output Voltage Difference (see IMPORTANT NOTE below)	Any input to any output shield or any shield to case, 60 Hz			24 V RMS 34 V peak



TEST CIRCUIT 1



TEST CIRCUIT 2



TEST CIRCUIT 3

All minimum and maximum specifications are guaranteed. Unless noted otherwise, all specifications apply at 25°C. Specifications subject to change without notice. All information herein is believed to be accurate and reliable, however no responsibility is assumed for its use nor for any infringements of patents which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Jensen Transformers, Inc.

IMPORTANT NOTE: THIS PRODUCT IS NOT INTENDED FOR USE IN CIRCUMSTANCES WHERE THE DC OR PEAK AC VOLTAGE BETWEEN INPUT AND OUTPUT CONNECTIONS EXCEEDS 34 VOLTS OR WHERE ITS FAILURE COULD CAUSE INJURY OR DEATH.

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