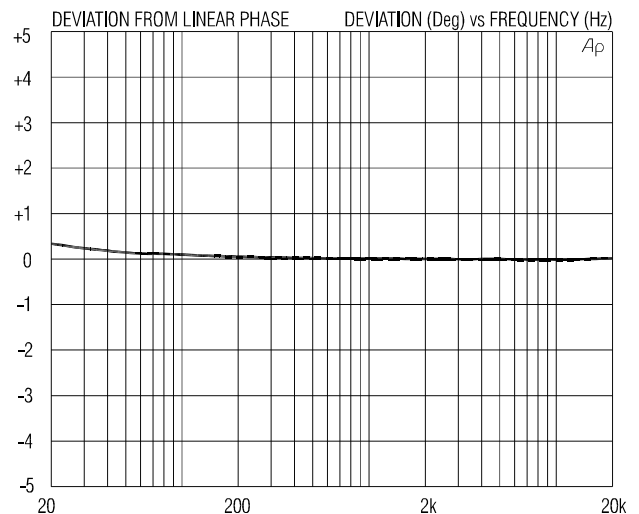
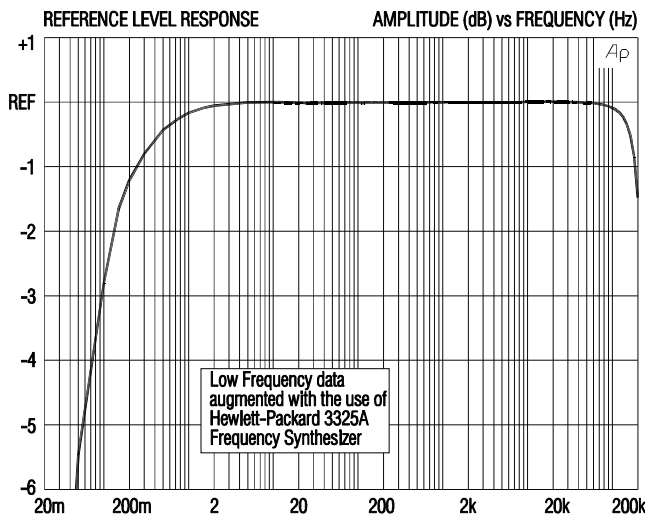
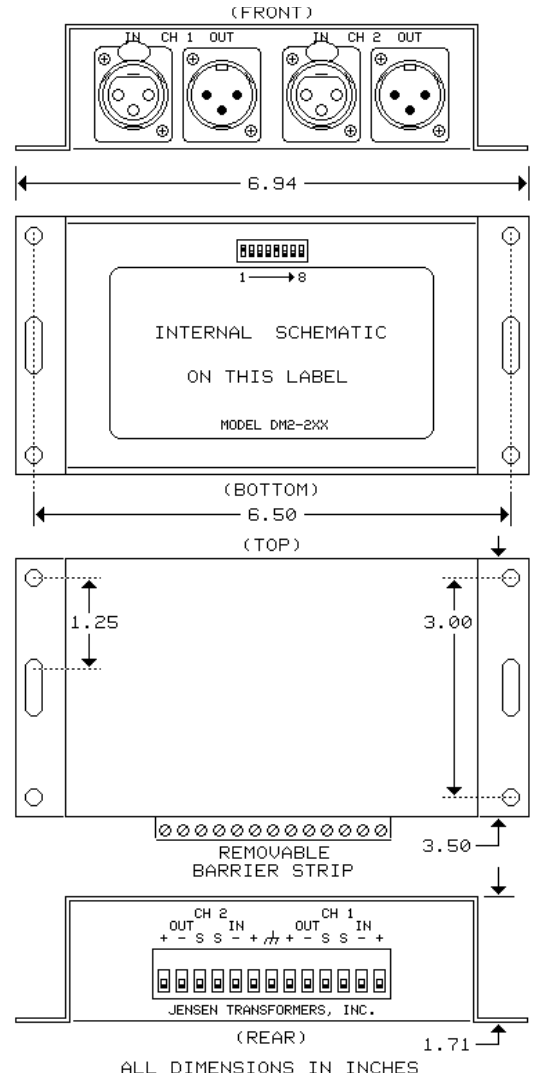


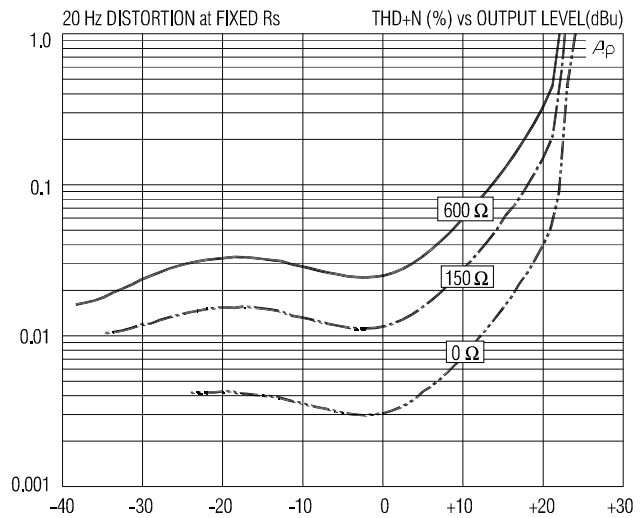
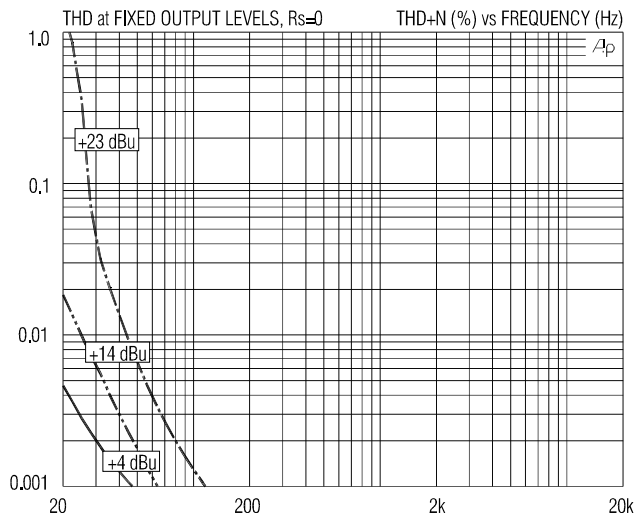
DUAL LINE OUTPUT ISOLATOR

DRIVE BAL or UNBAL INPUTS from BAL or UNBAL OUTPUTS

- Distortion 0.004% typ at 20 Hz and +4 dBu output level
- Wide bandwidth: -3 dB at 0.10 Hz and 15 MHz
- Handles levels up to +22 dBu at 20 Hz and +26 dBu at 30 Hz
- Excellent time domain performance: DLP 0.3° typ 20 Hz to 20 kHz
- Integral ground option switches solve other equipment problems

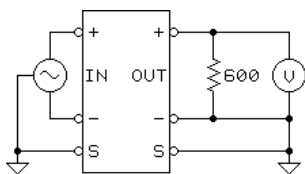
This isolator allows any output, balanced or unbalanced, to drive either balanced or unbalanced inputs while avoiding ground loop problems. It has no input or output cable length restrictions. Its 80% nickel transformers make it excellent for use with relatively high output impedance sources, such as consumer electronics.



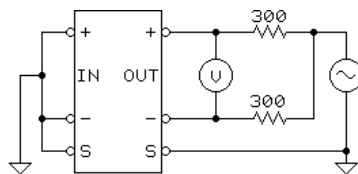


DM2-2XX SPECIFICATIONS (all levels are output unless noted)

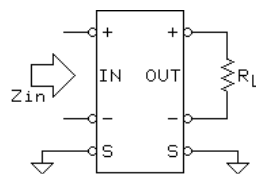
PARAMETER	CONDITIONS	MINIMUM	TYPICAL	MAXIMUM
Input impedance, Z_i	20 Hz to 20 kHz, 0 dBu, test circuit 3, $R_L = 600 \Omega$	670 Ω	680 Ω	690 Ω
	1 kHz, 0 dBu, test circuit 3, $R_L = 20 \text{ k}\Omega$		18.3 k Ω	
Voltage gain	1 kHz, 0 dBu, test circuit 1, $R_s=0 \Omega$	-1.2 dB	-1.1 dB	-0.9 dB
Magnitude response, referred to 1 kHz	20 Hz, 0 dBu, test circuit 1, $R_s=0 \Omega$	-0.1 dB	-0.02 dB	± 0.0 dB
	20 kHz, 0 dBu, test circuit 1, $R_s=0 \Omega$	-0.1 dB	± 0.00 dB	+0.1 dB
Deviation from linear phase (DLP)	20 Hz to 20 kHz, 0 dBu, test circuit 1, $R_s=0 \Omega$		+0.3 / -0°	$\pm 1.0^\circ$
Distortion (THD)	1 kHz, +4 dBu, test circuit 1, $R_s=0 \Omega$		<0.001%	
	20 Hz, +4 dBu, test circuit 1, $R_s=0 \Omega$		0.004%	0.03%
Maximum output level	20 Hz, 1% THD, test circuit 1, $R_s=0 \Omega$	+20 dBu	+22 dBu	
Common-mode rejection ratio (CMRR)	60 Hz, test circuit 2		110 dB	
	3 kHz, test circuit 2	75 dB	85 dB	
Recommended Source Impedance	Output impedance of device connected to ISO-MAX input	0 Ω	600 Ω	2 k Ω
Recommended Load Impedance	Input impedance of device connected to ISO-MAX output	150 Ω	20 k Ω	∞
Output impedance, Z_o	20 Hz to 20 kHz, 0 dBu, test circuit 4		80 Ω	
DC resistance	input or output		40 Ω	
	input to output, 1 kHz		28 nF	
Capacitance	input or output to chassis, 1 kHz		70 pF	
	operation or storage	0° C		70° C
Input to Output Voltage Difference (see IMPORTANT NOTE below)	any shield to any shield or any shield to chassis, 60 Hz			24 V RMS 34 V peak



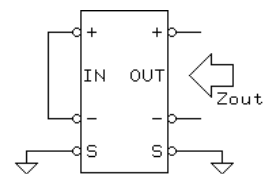
TEST CIRCUIT 1



TEST CIRCUIT 2



TEST CIRCUIT 3



TEST CIRCUIT 4

All minimum and maximum specifications are guaranteed. Unless noted otherwise, all specifications apply at 25°C. Specifications subject to change without notice. All information herein is believed to be accurate and reliable, however no responsibility is assumed for its use nor for any infringements of patents which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Jensen Transformers, Inc.

IMPORTANT NOTE: THIS PRODUCT IS NOT INTENDED FOR USE IN CIRCUMSTANCES WHERE THE DC OR PEAK AC VOLTAGE BETWEEN INPUT AND OUTPUT CONNECTIONS EXCEEDS 34 VOLTS OR WHERE ITS FAILURE COULD CAUSE INJURY OR DEATH.

JENSEN TRANSFORMERS, INC., 7135 Hayvenhurst Avenue, Van Nuys, CA 91406-3807, USA
(818) 374-5857 • FAX (818) 374-5856 • www.jensen-transformers.com